N32G05x Series Errata Manual V1.1.0

Content

Nations Technologies Inc.
Tel：+86-755-86309900
Email：info@nationstech.com
Address: Nations Tower, #109 Baoshen Road, Hi-tech Park North.
Nanshan District, Shenzhen, 518057, P.R.China

1 Errata List

Table 1-1 Errata Overview

Errata link		Chip version
		C version
chapter 2: Reset and clock control (RCC)	chapter 2.1: HSE not fully started up, HSE_ready bit ready issue	●
	chapter 2.2: LSI ready bit ready issue	●
chapter 3: Real Time Clock(RTC)	chapter 3.1: Tamper interrupt does not meet expectations	●
chapter 4: Analog to Digital Conversion(ADC)	chapter 4.1: When the ENDC flag is set, immediately read the ADC data register value for abnormal issues	●

●：There is this issue

-：There is no such issue

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2 Reset and clock control（RCC）

2.1 HSE not fully started up, HSE_ready bit ready issue

Description

After enabling HSE, HSE did not fully vibrate, but HSE READY was set.

Solution

1. Capture the HSE/128 clock using TIM2_CH4, determine that HSE is fully oscillating, and add this code to the SDK. The maximum waiting time for determining whether HSE has fully started oscillating is 200ms. If it exceeds 200ms, HSI will be used as the system clock.

2.2 LSI ready bit ready issue

Description

The LSI ready bit cannot clear the issue in RUN mode.

Reason

1. In RUN mode, the LSI remains in the enabled state, the LSI enable bit is invalid, and the LSI remains in the ready state.

3 Real Time Clock(RTC)

3.1 Tamper interrupt does not meet expectations

Description

RTC intrusion pin rising/falling edge triggering tamper interrupt does not meet expectations, and there may be situations where it cannot be triggered or triggered incorrectly.

Solution

The RTC intrusion pin requires adding up/down resistors to the outer board level circuit.

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4 Analog to Digital Conversion(ADC)

4.1 When the ENDC flag is set, immediately read the ADC data register value for abnormal issues

Description

After ENDC is set, immediately read the ADC data register, which may read the result of the previous conversion.

Solution

1. After the ENDC flag is set, delay by 8 ADC_CK clocks before reading the ADC data register;
2. In some scenarios, the ENDCA flag is used instead of the ENDC flag.

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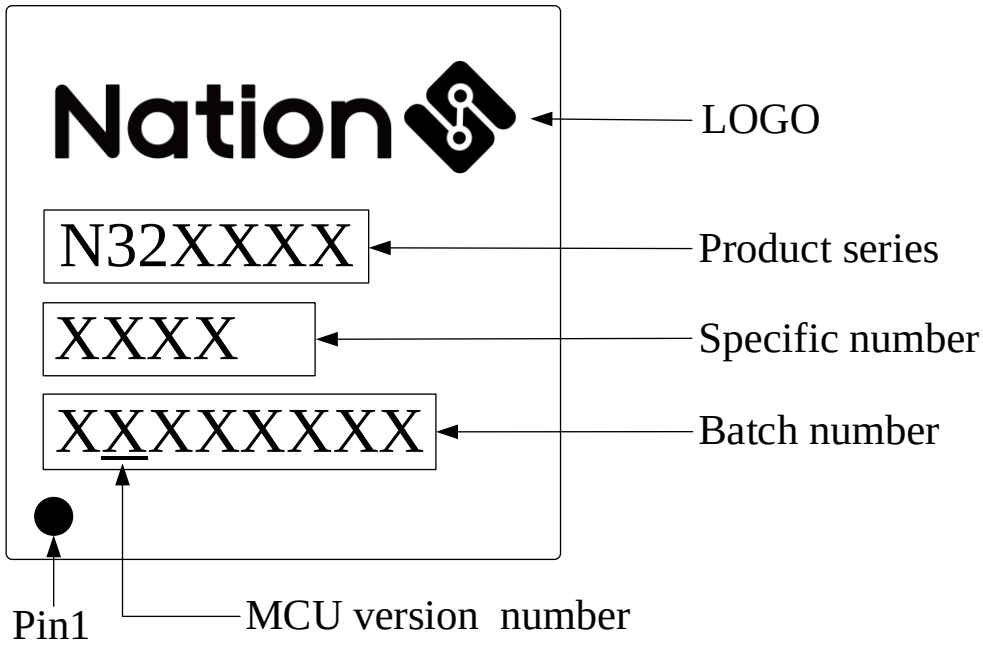
5 Chip Marking Information and Version Description



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6 Version history

Data	Version	Notes
2024.7.18	V1.0.0	Initial version
2024.11.29	V1.1.0	1. Update marking information

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