

User Guide

N32G030K6Q7-1-STB Development Board Hardware Usage Guide

Introduction

The purpose of this document is to allow users to quickly familiarize themselves with the N32G030K6Q7-1-STB development board, understand the functions, instructions and precautions of the development board, so as to conduct MCU debugging and development based on the development board.

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1 Hardware Development Instructions

1.1 Briefly

The N32G030K6Q7-1-STB development board is used for sample development of 32-bit N32G030K6Q7-1series chips of Nations Technology Co., Ltd. This document describes the functions, usage instructions and precautions of the N32G030K6Q7-1-STB development board in detail.

1.2 Development board function

The main MCU chip of the development board is N32G030K6Q7-1, and it is packaged with QFN32 pins. The development board connects all functional interfaces to facilitate customer development.

1.3 Development board layout

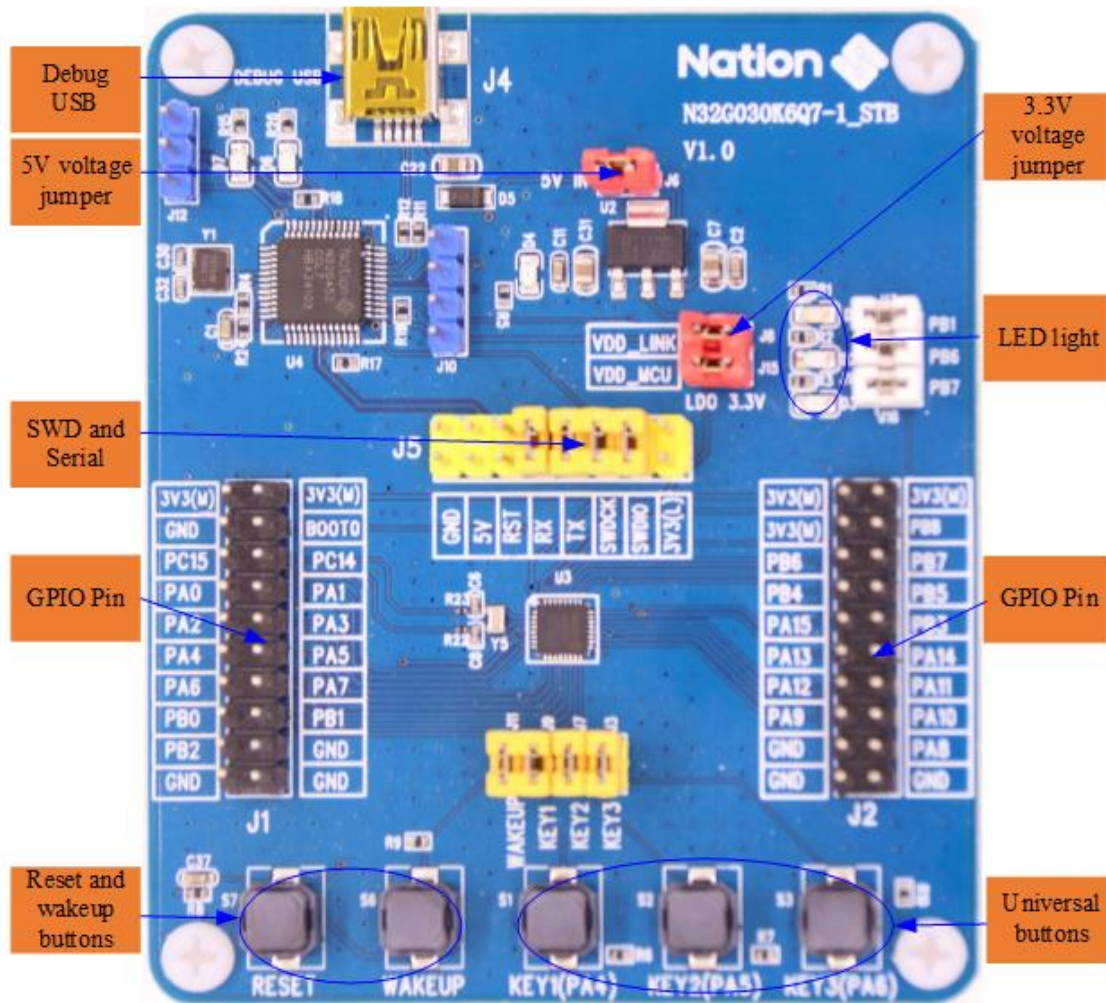


Figure 1-1 Development board layout

1) Power supply for the development board

The development board can be powered by DEBUG USB (J4), and connected to 3.3V LDO input port through J6 jumper.

2) Debug USB (J4)

Through the DEBUG USB interface of NS-LINK chip (U4), it can provide MCU program download debugging function, or connect the serial port of MCU to provide USB serial port function.

3) SWD interface and Serial port (J5)

SWD interface: SWDIO and SWDCK, used to download and debug the main MCU program, you can use ULINK2 or JLINK to download and debug the MCU, or you can short the SWDIO signal pin and the SWDCK signal pin with the jumper cap, and download the MCU through DEBUG USB debugging.

Serial port: MCU_TX and MCU_RX, used as serial port external signal, MCU's PA9 (TX) and PA10 (RX) are used as serial port, which can be connected to serial port devices separately, or the jumper cap can short the MCU_TX signal pin and the MCU_RX signal pin, Through the NS-LINK on the development board, the USB port is converted into a serial port, which is convenient for customers to use.

4) Reset and Wake Buttons (S7, S6)

S7 and S6 are the reset button and wake-up button respectively, Connect the chip's NRST pin and PA0-WKUP pin respectively for chip reset and wake-up functions.

5) Universal keys (S1, S2, S3)

S1, S2, and S3 are general buttons, which are connected to the pins PA4, PA5 and PA6 of the chip respectively.

6) BOOT (J1 PIN4)

J1 PIN4 is BOOT0 pin, which can be shorted to power and ground through jumper caps as needed.

7) GPIO (J1, J2)

The GPIO interface of the chip is all led out, and the 3.3V voltage and GND pins are also reserved on the pins, which is convenient for testing. For the specific definition of the interface, please refer to "DS_N32G030 Series datasheet".

1.4 Development Board Jumper Instructions

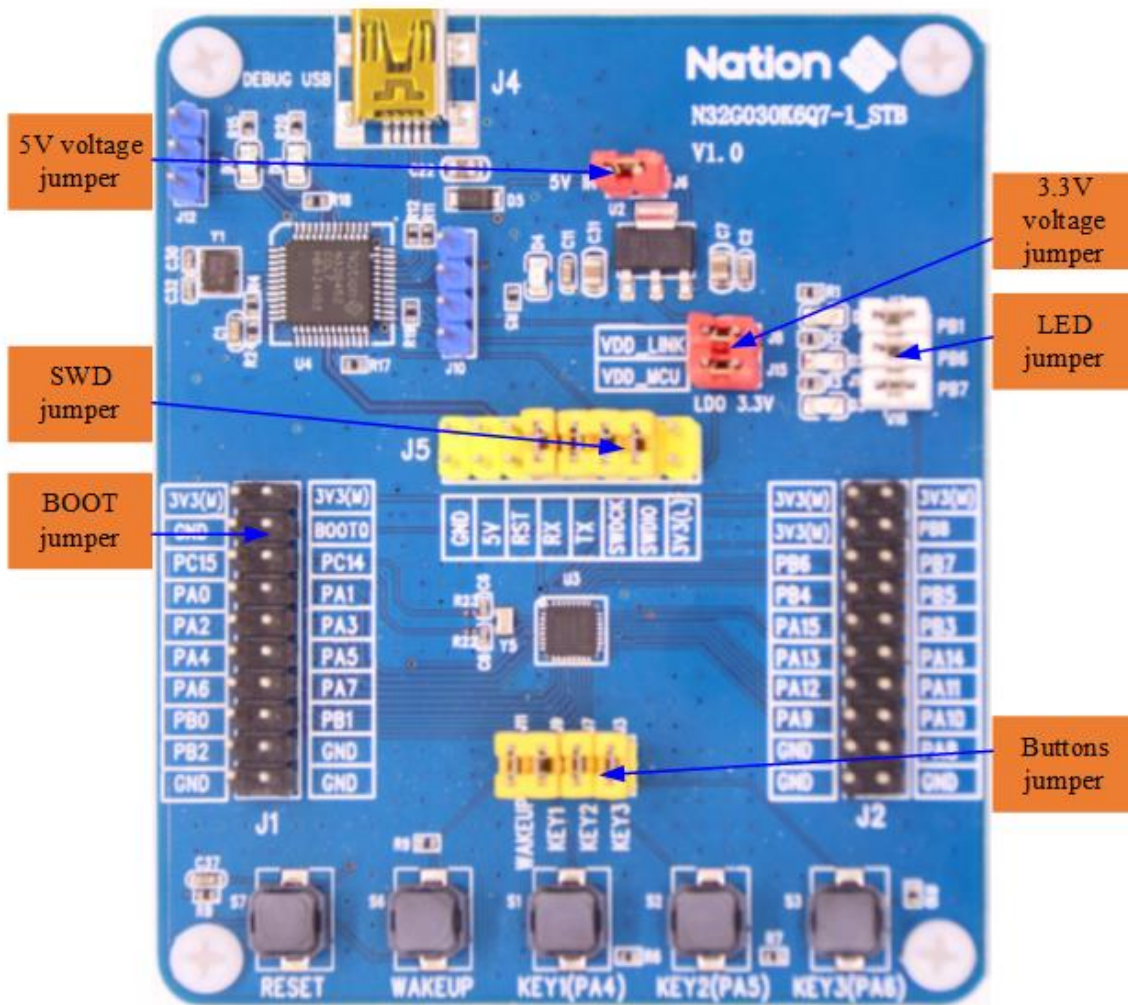


Figure 1-2 Development Board Jumper Description

Table 1-1 Development Board Jumper Description List

No.	Jumper bit number	Jumper function	Instructions for use
1	J6	5V input voltage jumper	The jumper J6 is used to connect the USB ports J4 to supply power to the LDO3.3V input port.
2	J8、J15	3.3V Power supply jumper	J8: Power supply 3.3V to NS-LINK MCU chip. J15: Power supply 3.3V to the main MCU chip.
3	J5	SWD jumper	Use NS-LINK to download the program to the MCU through the USB DEBUG port, you need to short-circuit the SWDIO signal pin and the SWDCK signal pin.
	J5	Serial jumper	When using NS-LINK as a serial port through the USB DEBUG port, you need to short-circuit the MCU_TX signal pin and the MCU_RX signal pin.
4	J1 PIN4	BOOT jumper	J1 PIN4: BOOT0.
5	J13、J14、J16	LED light jumper	LED light jumper to disconnect GPIO from LED J13: D1(PB1) J14: D2(PB6) J16: D3(PB7)
6	J3、J7、J9、J11	button jumper	Button jumper to disconnect the GPIO from the button J9: KEY1(PA4) J7: KEY2 (PA5) J3: KEY3(PA6) J11: WAKEUP(PA0)

1.5 Development board schematic

The schematic diagram of the N32G030K6Q7-1-STB development board is described as follows (For details, please refer to "N32G030K6Q7-1-STB_V1.0").

1) MCU connection

Refer to Figure 1-3 for the schematic diagram of the MCU connection. Each VDD pin of the MCU is

[illegible]

2) Power Design

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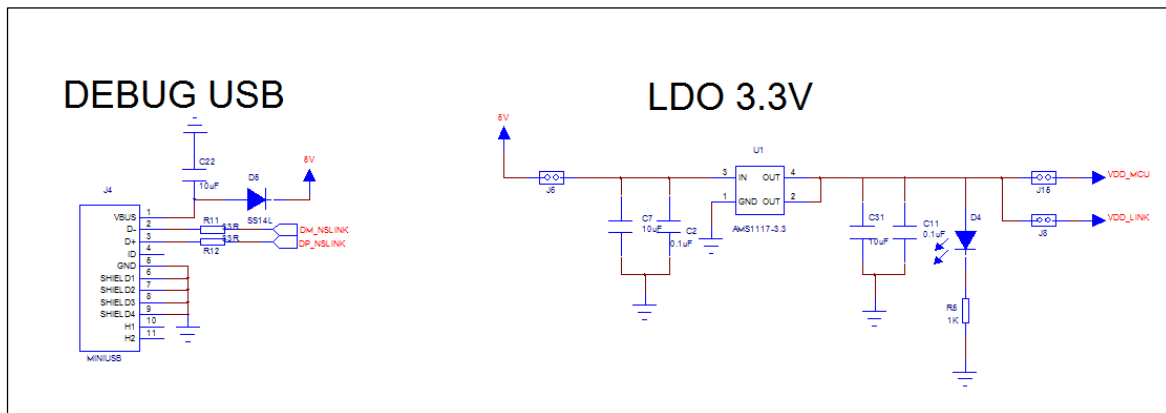


Figure 1-4 Power Design

3) button design

Refer to Figure 1-5 for the schematic diagram of the key design. There are a total of 5 keys, which are the three general keys, the MCU wake-up key and reset key.

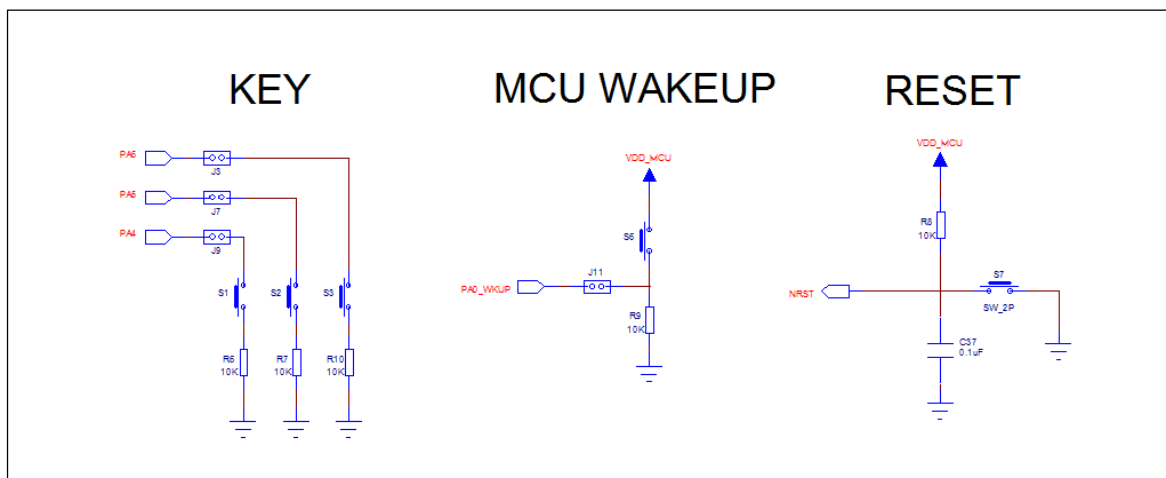


Figure 1-5 Button Design

4) LED light design

Refer to Figure 1-6 for the schematic diagram of LED light design. There are a total of 5 LED lights. D1, D2, and D3 are connected to PB1, PB6 and PB7 of the main MCU respectively, which can be used for debugging. D6 and D7 are used for NS-LINK MCU control to monitor the running status of NS-LINK.

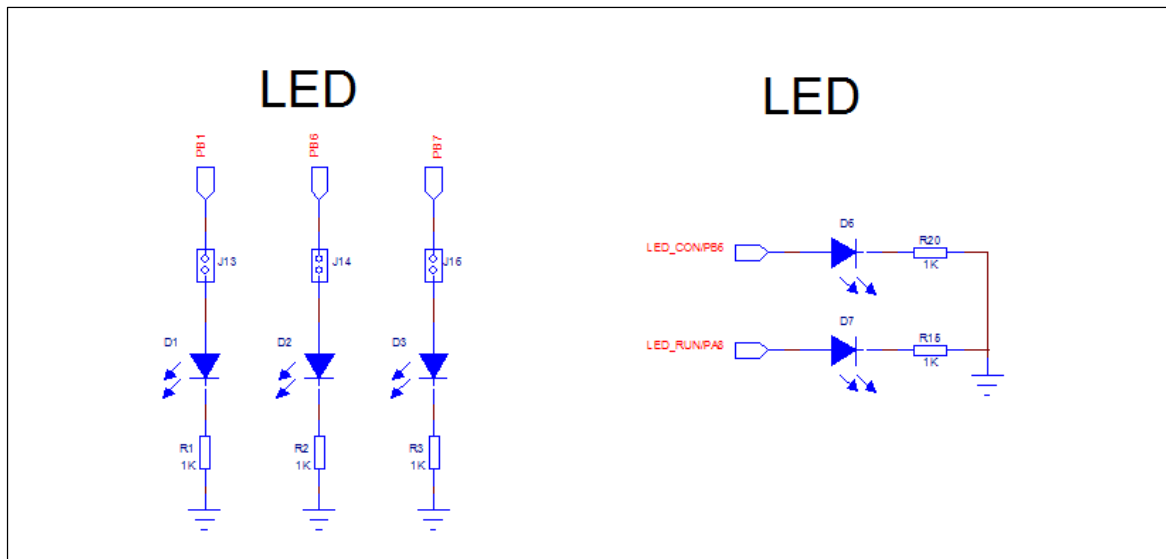


Figure 1-6 LED Light Design

5) crystal

Refer to Figure 1-7 for the crystal connection diagram. The chip has 8MHz external crystals.

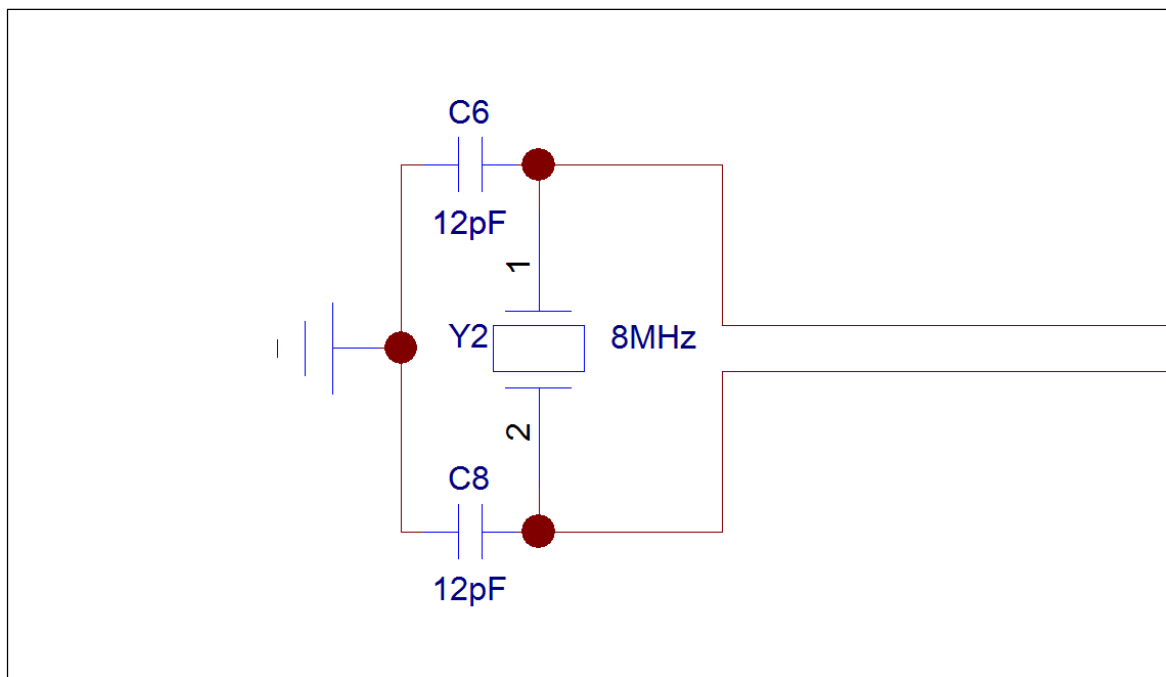


Figure 1-7 crystal design

6) NS-LINK

Refer to Figure 1-8 for the schematic diagram of NS-LINK. Users can directly connect the USB cable to

The image displays several circuit diagrams for the NS-LINK system:

- MCU:** A detailed schematic of the microcontroller unit. It includes a USB connector (J1) with pins for VBUS, D-, D+, GND, and a 5V pin. The MCU is connected to various peripheral components, including a 10k resistor (R1), a 100nF capacitor (C1), a 10k resistor (R2), a 100nF capacitor (C2), a 10k resistor (R3), a 100nF capacitor (C3), a 10k resistor (R4), a 100nF capacitor (C4), a 10k resistor (R5), a 100nF capacitor (C5), a 10k resistor (R6), a 100nF capacitor (C6), a 10k resistor (R7), a 100nF capacitor (C7), a 10k resistor (R8), a 100nF capacitor (C8), a 10k resistor (R9), a 100nF capacitor (C9), a 10k resistor (R10), a 100nF capacitor (C10), a 10k resistor (R11), a 100nF capacitor (C11), a 10k resistor (R12), a 100nF capacitor (C12), a 10k resistor (R13), a 100nF capacitor (C13), a 10k resistor (R14), a 100nF capacitor (C14), a 10k resistor (R15), a 100nF capacitor (C15), a 10k resistor (R16), a 100nF capacitor (C16), a 10k resistor (R17), a 100nF capacitor (C17), a 10k resistor (R18), a 100nF capacitor (C18), a 10k resistor (R19), a 100nF capacitor (C19), a 10k resistor (R20), a 100nF capacitor (C20), a 10k resistor (R21), a 100nF capacitor (C21), a 10k resistor (R22), a 100nF capacitor (C22), a 10k resistor (R23), a 100nF capacitor (C23), a 10k resistor (R24), a 100nF capacitor (C24), a 10k resistor (R25), a 100nF capacitor (C25), a 10k resistor (R26), a 100nF capacitor (C26), a 10k resistor (R27), a 100nF capacitor (C27), a 10k resistor (R28), a 100nF capacitor (C28), a 10k resistor (R29), a 100nF capacitor (C29), a 10k resistor (R30), a 100nF capacitor (C30), a 10k resistor (R31), a 100nF capacitor (C31), a 10k resistor (R32), a 100nF capacitor (C32), a 10k resistor (R33), a 100nF capacitor (C33), a 10k resistor (R34), a 100nF capacitor (C34), a 10k resistor (R35), a 100nF capacitor (C35), a 10k resistor (R36), a 100nF capacitor (C36), a 10k resistor (R37), a 100nF capacitor (C37), a 10k resistor (R38), a 100nF capacitor (C38), a 10k resistor (R39), a 100nF capacitor (C39), a 10k resistor (R40), a 100nF capacitor (C40), a 10k resistor (R41), a 100nF capacitor (C41), a 10k resistor (R42), a 100nF capacitor (C42), a 10k resistor (R43), a 100nF capacitor (C43), a 10k resistor (R44), a 100nF capacitor (C44), a 10k resistor (R45), a 100nF capacitor (C45), a 10k resistor (R46), a 100nF capacitor (C46), a 10k resistor (R47), a 100nF capacitor (C47), a 10k resistor (R48), a 100nF capacitor (C48), a 10k resistor (R49), a 100nF capacitor (C49), a 10k resistor (R50), a 100nF capacitor (C50), a 10k resistor (R51), a 100nF capacitor (C51), a 10k resistor (R52), a 100nF capacitor (C52), a 10k resistor (R53), a 100nF capacitor (C53), a 10k resistor (R54), a 100nF capacitor (C54), a 10k resistor (R55), a 100nF capacitor (C55), a 10k resistor (R56), a 100nF capacitor (C56), a 10k resistor (R57), a 100nF capacitor (C57), a 10k resistor (R58), a 100nF capacitor (C58), a 10k resistor (R59), a 100nF capacitor (C59), a 10k resistor (R60), a 100nF capacitor (C60), a 10k resistor (R61), a 100nF capacitor (C61), a 10k resistor (R62), a 100nF capacitor (C62), a 10k resistor (R63), a 100nF capacitor (C63), a 10k resistor (R64), a 100nF capacitor (C64), a 10k resistor (R65), a 100nF capacitor (C65), a 10k resistor (R66), a 100nF capacitor (C66), a 10k resistor (R67), a 100nF capacitor (C67), a 10k resistor (R68), a 100nF capacitor (C68), a 10k resistor (R69), a 100nF capacitor (C69), a 10k resistor (R70), a 100nF capacitor (C70), a 10k resistor (R71), a 100nF capacitor (C71), a 10k resistor (R72), a 100nF capacitor (C72), a 10k resistor (R73), a 100nF capacitor (C73), a 10k resistor (R74), a 100nF capacitor (C74), a 10k resistor (R75), a 100nF capacitor (C75), a 10k resistor (R76), a 100nF capacitor (C76), a 10k resistor (R77), a 100nF capacitor (C77), a 10k resistor (R78), a 100nF capacitor (C78), a 10k resistor (R79), a 100nF capacitor (C79), a 10k resistor (R80), a 100nF capacitor (C80), a 10k resistor (R81), a 100nF capacitor (C81), a 10k resistor (R82), a 100nF capacitor (C82), a 10k resistor (R83), a 100nF capacitor (C83), a 10k resistor (R84), a 100nF capacitor (C84), a 10k resistor (R85), a 100nF capacitor (C85), a 10k resistor (R86), a 100nF capacitor (C86), a 10k resistor (R87), a 100nF capacitor (C87), a 10k resistor (R88), a 100nF capacitor (C88), a 10k resistor (R89), a 100nF capacitor (C89), a 10k resistor (R90), a 100nF capacitor (C90), a 10k resistor (R91), a 100nF capacitor (C91), a 10k resistor (R92), a 100nF capacitor (C92), a 10k resistor (R93), a 100nF capacitor (C93), a 10k resistor (R94), a 100nF capacitor (C94), a 10k resistor (R95), a 100nF capacitor (C95), a 10k resistor (R96), a 100nF capacitor (C96), a 10k resistor (R97), a 100nF capacitor (C97), a 10k resistor (R98), a 100nF capacitor (C98), a 10k resistor (R99), a 100nF capacitor (C99), a 10k resistor (R100), a 100nF capacitor (C100).
- DEBUG USB:** A schematic showing a USB connector (J1) connected to a USB-to-serial converter (U1) and a USB-to-serial converter (U2). The USB-to-serial converter (U1) is connected to a USB-to-serial converter (U2) and a USB-to-serial converter (U3).
- HEADER:** A schematic showing a multi-pin header (J1) connected to a multi-pin header (J2) and a multi-pin header (J3).
- LED:** A schematic showing an LED (L1) connected to a multi-pin header (J1) and a multi-pin header (J2).

- **Description of peripheral devices:**

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2 Version history

Version	Date	Modify
V1.0	2020-07-25	Initial version

3 Notice

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