

### DRIVEN BY INNOVATION



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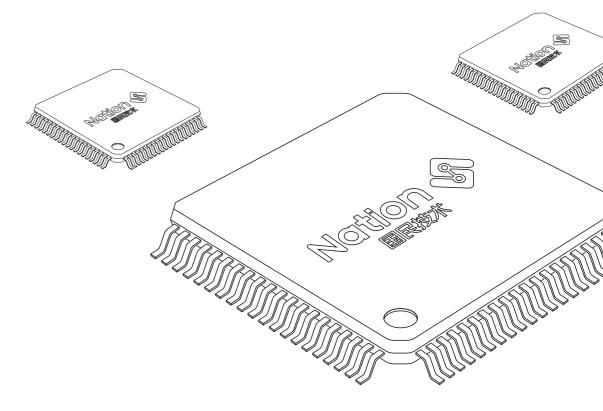
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Stock Code:300077

## **PRODUCT SELECTION GUIDE**







# **Nations Technologies is committed to providing IC** and solutions for people,

Making lives Safer, Simpler and Smarter









### **Qualifications and Honors**

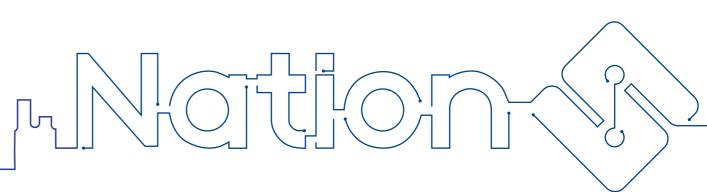
- · Deputy Chairman member unit of China Association for Public Companies
- · National High-tech Enterprise
- · National-level postdoctoral programme
- · Leading enterprise in Shenzhen's independent innovation industries
- ·Shenzhen R&D Center for Engineering and Technology
- · Shenzhen R&D Center for Information Security IC Technology
- ·Shenzhen Key Laboratory

### **Service Capability**

- ·The Globalization of R&D
- · Localized technical service team
- · Strategic partnerships with world-class wafer vendors

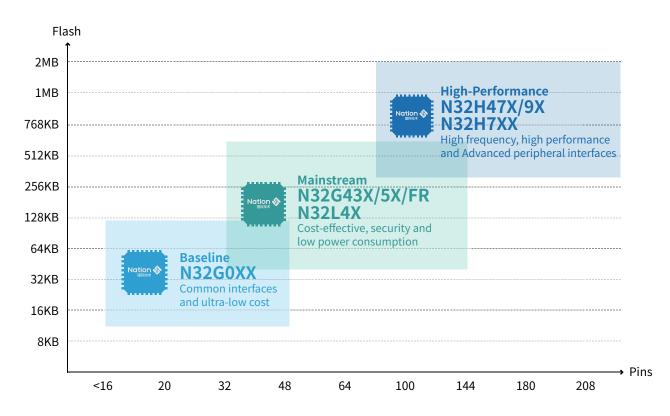
### **Technical Competence**

- ·Own more than 1,400 international and domestic patents, including more than 1,000 invention patents
- ·Won the "China Patent Gold Award" in 2017. Won 9 of China Patent Excellence Awards for several years
- ·Own 60 technical standards. RCC technology had become a national standard in May,2017. Own the new generation trusted computing ISO / IEC international standard

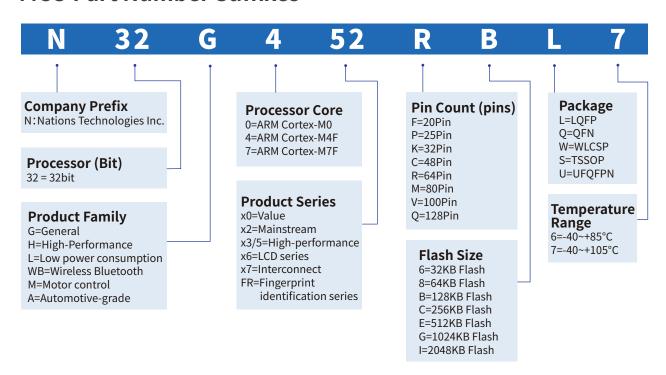


# **Marketing Strategy Of MCUs**

Sustainable Innovation, Providing More than **100** Product Models For Various Industries



### **MCU Part Number Suffixes**



## Security IC

			71					Oper	S	Tim	er		ADC					Three		Con	neci	tivity			ESD	(HM)		Power sumpt		Secu	Crypt			(	Certifi	cation		
Series	Commercial Product Code	CPU Core	Frequency (MHz)	Flash (KB)	ROM (KB)	EEPROM (KB)	SRAM (KB)	ating temperature	Supply voltage/	SysTick	RTC	PWM	Nb Resolution	PWM	DAC	Capture	LCD	hree track magnetic head	ISO7816	UART	SPI/I <sup>2</sup> S	USB Device	ISO14443	DMA / PWM	Contact(KV)	Contactless(KV)	PowerDown	Standby	Run(Typ)	Security Management	Cryptographic algorithm	Package	state Cryptography Administr	China Information Technology Security Evaluation Center	Information Security Certification Center of China	Bank Card Test Center	NIST	USB -IF
	N32S032	ARM Cort	80	320	-	_	21	30 <sub>-40~+85°C</sub>	1.8V~	1	1	8	1x12bit	12	1 .	5 1	-	-	1	3	2/-	2 1	-	1/8	<u>+</u> 4	_	0.1uA	80uA	110uA /MHz	•		QFN20/SOP	Level II I	EAL5+	-	-	FIPS 140-2 CAVP	USB IF Cer tification
	N32S033	ex- M0	80	512	-	-	33	30 6	5.5\( 5	1	1	1	1x10bit	10	-	5 1	-	-	1	2	2/1	2 1	-	1/6	<u>+</u> 4	_	0.1uA	80uA	125uA /MHz	•	AES/DES/ 3DES/SM1/	QFN48 QFN32 SOP8	LevelII	EAL4+	-	-	-	B IF ication
Multi-function Security IC	Z32HUB	ARM Cort ex- M0	60	320	-	_	16	22 +85°C	2.7V~5.5V/	1	-	1	-	-	-	- 1	-	-	-	1	1/-	- 1	-	2/1	<u>+</u> 4	-	1uA	130uA	500uA /MHz	•	SM4、RSA/ ECC/SM2/ SM9、SHA1/ 224/256/384/	QFN32	Level II [	EAL4+	-	-	FIPS 140-2 CAVP	USB IF Certification
tion Sec		ARM						33 25	2.4V				1x12bit	3					3		3/–							120	20		512/SM3	QFN68				Terminal assessme payment security a	FIPS	Us Cer ti
urity IC	Z32HUA	Cort ex- M0	80	512	-	-	51	20 C	2.4V~5.5V/	1	1	-	-	-	1	-   -	-	-	-	1	1/–	1 1	-	1/8	<u>+</u> 4	-	1uA	130 uA	20 mA	٠		QFN32	Level II I	EAL4+	-	Terminal IC security assessment, personal payment terminal security assessment	FIPS 140-2 CAVP	SB IF fication
	Z8IDA	Zi80 51- SC	32	-	96	32	8	-25~+85°C	2.7V~5.5V/	-	-	-	1x10bit	3	-	-   -	.   -	-	1	-	_	1 -	-	-	-	-	-	50 uA	4mA	•	DES/3DES/ SM4/SSF33, RSA/SM2, ECC,SHA1/ SHA256/ SM3	DFN8 SOP8	Level II	-	-	-	-	-
command security IC		Zi80 51- SC	2	48	-	-	3.25	-20~+70°C	2.4V~3.6V/	_	1	_	1x10bit	3	-			-	_	1	-		_	_	<u>+</u> 4	-	< 0.8 uA	< 2uA @32.7 68KHz	<5uA @32.7 68KHz : <130 uA	•	SM3	Die	Levell	-	-	-	-	-
High-capacity security IC	Z32HM	M4K RISC	60	1024	. <del>-</del>	-	48	-25~+85°C	2.7V~5.5V/	-	-	_	-	-	-	-   -	_	-	2	1	1/-		-	4	-	-	-	<100 uA	7mA @Core 60MHZ 3.5mA @Core 30MH	_	DES/3DES/AES/ SM1/SM4/SM7 /SSF33,RSA/ SM2,SHA 1/ 224/256/SM3	WLCSP SOP8 DFN8	Level II	EAL4+	EAL4+	_	FIPS 140-2 CAVP	-
Smart card security IC	Z32HCD2	ARM Cort	50	-	256	40	11	4 -25~+	2.4V~	<u>-</u>	-	-	<del>-</del>	_			· <u>-</u>	_	1	_	_		1	_	<u>+</u> 4	<u>±</u> 6	<200 uA	<200 uA	6mA	_	DES/3DES/ SM1/SM4/ SM7/SSF33,	Strip	Level II	_	EAL4+	PBOC3.0 (Debit/C QPBOC extension of the third general security card (included) tharacteristics)	FIPS 140-2 CAVP	
card ty IC	Z32HCD2S	ex- M0	50	-	320	80	11	4 °C	5.5V/	_	-	-	-	-	-	-   -	-	-	1	_	-		1	-	<u>+</u> 4	<u>±</u> 6	<200 uA	<200 uA	6mA	_	RSA/SM2,ECC, SHA1/256/ SM3	- Cu.,p	Leveri			ebit/Credit, QPBOC/ ension), COS detection generation social generation social gincluding physical ticsi	CAVP	

Note: "·" means support "-" means "not support"

## General MCU

							Ope	Time		PWM	ADC									C	onne	ctivi	ty _								Спу	
Series	Commercial Product Code	Core	Frequency (MHz)	Flash (KB)	SRAM (KB)	1/0	Supply voltage/ Operating temperature	Time		com	Nb Resolution	PWM	DAC	OPAMP	COMP	LPRCNT	TSC	USART/ISO7816	UART	LPUART	SPI/I <sup>2</sup> S	QSPI	I <sup>2</sup> C	USB Device	CAN		DMA / PWM	SEGMENT LCD	ETH	DVP	Cryptographic algorithm	Package
	N32G452CBL7		144	128	80	37		8 1	2	3 6	2x12bit	10	2x12-bit	-	-	-	-	3	3	-	3/2	1 <sup>1</sup>	3	1	2 -	- 2/	′16	-	_	_		LQFP48
	N32G452CCL7		144	256	144	37		8 1	2	3 6	2x12bit	10	2x12-bit	_	-	-	-	3	3	-	3/2	1 <sup>1</sup>	3	1	2 -	- 2/	′16	-	_	-		LQFP48
	N32G452RBL7		144	128	80	51		8 1	2	4 12	2x12bit	16	2x12-bit	_	-	-	-	3	4	-	3/2	1	4	1	2	2/	′16	_	-	-		LQFP64
	N32G452RCL7		144	256	144	51		8 1	2	4 12	2x12bit	16	2x12-bit	-	_	-	-	3	4	-	3/2	1	4	1	2	2/	<sup>′</sup> 16	-	_	-		LQFP64
_	N32G452REL7		144	512	144	51		8 1	2	4 12	2x12bit	16	2x12-bit	_	-	_	-	3	4	-	3/2	1	4	1	2	2/	′16	_	-	-	DE	LQFP64
N32G452	N32G452MBL7		144	128	80	67		8 1	2	4 12	2x12bit	16	2x12-bit	_	_	-	-	3	4	-	3/2	1	4	1	2	2/	<b>′</b> 16	-	_	-	DES/3DES、	LQFP80
G45	N32G452MCL7		144	256	144	67		8 1	2	4 12	2x12bit	16	2x12-bit	_	-	-	-	3	4	-	3/2	1	4	1	2	2/	′16	_	-	-	DES	LQFP80
Ň	N32G452MEL7		144	512	144	67		8 1	2	4 12	2x12bit	16	2x12-bit	_	_	-	-	3	4	- 1	3/2	1	4	1	2	2/	′16	-	_	-		LQFP80
	N32G452VCL7		144	256	144	80		8 1	2	4 12	2x12bit	16	2x12-bit	_	-	-	-	3	4	-	3/2	1	4	1	2	2/	′16	_	-	-	AES,	LQFP100
	N32G452VEL7		144	512	144	80		8 1	2	4 12	2x12bit	16	2x12-bit	-	_	_	-	3	4	- 1	3/2	1	4	1	2	2/	<b>′</b> 16	-	_	-		LQFP100
	N32G452QCL7		144	256	144	97		8 1	2	4 12	2x12bit	18	2x12-bit	_	-	-	_	3	4	-	3/2	1	4	1	2	2/	′16	_	-	-	IA1/	LQFP128
	N32G452QEL7		144	512	144	97		8 1	2	4 12	2x12bit	18	2x12-bit	_	-	-	-	3	4	- [	3/2	1	4	1	2	2/	′16	-	_	-	SHA1/SHA224/SHA256、	LQFP128
	N32G455CBL7		144	128	80	37		8 1	2	3 6	4x12bit	16	2x12-bit	4	5	-	8	3	3	-	3/2	1 <sup>1</sup>	3	1	2 -	- 2/	16	-	_	-	222	LQFP48
	N32G455CCL7		144	256	144	37		8 1	2	3 6	4x12bit	16	2x12-bit	4	5	-	8	3	3	-	3/2	11	3	1	2 -	- 2/	16	-	_	-	1/S/	LQFP48
	N32G455RBL7		144	128	80	51		8 1	2	4 12	4x12bit	22	2x12-bit	4	7	-	16	3	4	_	3/2	1	4	1	2	2/	16	-	_	-	12	LQFP64
	N32G455RCL7	≱	144	256	144	51	1.8	8 1	2	4 12	4x12bit	22	2x12-bit	4	7	-	16	3	4	-	3/2	1	4	1	2	2/	16	-	_	-	56,	LQFP64
N S	N32G455REL7	ARM	144	512	144	51	1.8V~3.6V	8 1	2	4 12	4x12bit	22	2x12-bit	4	7	-	16	3	4	_	3/2	1	4	1	2	2/	16	-	-	_	SM1,	LQFP64
N32G455	N32G455MBL7	Cortex	144	128	80	67	.6V	8 1	2	4 12	4x12bit	33	2x12-bit	4	7	-	18	3	4	-	3/2	1	4	1	2	2/	16	-	_	-	7	LQFP80
55	N32G455MCL7	- 1	144	256	144	67		8 1	2	4 12	4x12bit	33	2x12-bit	4	7	-	18	3	4	_	3/2	1	4	1	2	2/	16	_	_	-	SM3	LQFP80
	N32G455MEL7	M4F	144	512	144	67	F~0	8 1	2	4 12	4x12bit	33	2x12-bit	4	7	-	18	3	4	-	3/2	1	4	1	2	2/	16	-	_	-	Ś	LQFP80
	N32G455VBL7	П	144	128	80	80	-40~+105	8 1	2	4 12	4x12bit	38	2x12-bit	4	7	-	24	3	4	_	3/2	1	4	1	2	2/	16	-	_	-	SM4、	LQFP100
	N32G455VCL7		144	256	144	80	ဂိ	8 1	2	4 12	4x12bit	38	2x12-bit	4	7	-	24	3	4	-	3/2	1	4	1	2	2/	16	-	_	-		LQFP100
	N32G455VEL7		144	512	144	80		8 1	2	4 12	4x12bit	38	2x12-bit	4	7	-	24	3	4	-	3/2	1	4	1	2	2/	16	-	_	-	SM7,	LQFP100
	N32G457RCL7		144	256	144	51		8 1	2	4 12	4x12bit	22	2x12-bit	4	7	_	16	3	4	-	3/2	1	4	1	2	2/	′16	-	1	Υ	MD5、	LQFP64
	N32G457REL7		144	512	144	51		8 1	2	4 12	4x12bit	22	2x12-bit	4	7	-	16	3	4	-	3/2	1	4	1	2	2/	'16	-	1	Υ		LQFP64
Z	N32G457MCL7		144	256	144	67		8 1	2	4 12	4x12bit	33	2x12-bit	4	7	-	18	3	4	-	3/2	1	4	1	2	2/	'16	-	1	Υ	CK	LQFP80
N32G457	N32G457MEL7		144	512	144	67		8 1	2	4 12	4x12bit	33	2x12-bit	4	7	-	18	3	4	-	3/2	1	4	1	2	2/	'16	-	1	Υ	C16/	LQFP80
457	N32G457VCL7		144	256	144	80		8 1	2	4 12	4x12bit	38	2x12-bit	4	7	-	24	3	4	-	3/2	1	4	1	2	2/	′16	-	1	Υ	CR (CR	LQFP100
	N32G457VEL7		144	512	144	80		8 1	2	4 12	4x12bit	38	2x12-bit	4	7	_	24	3	4	-	3/2	1	4	1	2	2/	'16	-	1	Υ	CRC16/CRC32	LQFP100
	N32G457QEL7		144	512	144	97		8 1	2	4 12	4x12bit	40	2x12-bit	4	7	_	24	3	4	-	3/2	1	4	1	2	2/	'16	-	1	Υ	,	LQFP128
	N32G4FRKCQ7		144	256	144	24		6 1	1	0 6	2x12bit	7	2x12-bit	-	-	-	2	1	3	_	2/1	1	3	1	1 -	- 2/	′16	-	_	-	TRNG	QFN32
z	N32G4FRKEQ7		144	512	144	24		6	1	0 6	2x12bit	7	2x12-bit	_	_	-	2	1	3	-	2/1	1	3	1	1 -	- 2/	′16	-	_	-	.,	QFN32
N32G4FR	N32G4FRHCQ7		144	256	144	32		7 ^	1	2 6	2x12bit	11	2x12-bit	_	_	-	5	2	4	_	3/2	1	4	1	2 -	- 2/	′16	-	-	Υ		QFN40
34 17	N32G4FRHEQ7		144	512	144	32		7 ′	1	2 6	2x12bit	11	2x12-bit	_	_	-	5	2	4	-	3/2	1	4	1	2 -	- 2/	′16	-	-	Υ		QFN40
20	N32G4FRREL7		144	512	144	51		8 -	1 2	4 12	2x12bit	16	2x12-bit	-	-	-	16	3	4	-	3/2	1	4	1	2	1 2/	′16	-	-	Υ		LQFP64
	N32G4FRMEL7		144	512	144	67		8	1 2	24 12	2x12bit	18	2x12-bit	_	_	-	18	3	4	-	3/2	1	4	1	2	1 2,	/16	-	-	Υ		LQFP80

### General MCU

	iciatineo						Op	Time	ar I	PWM	ADC									C	onne	ctivi	ty							Спу	
Series	Commercial Product Code	Core	Frequency (MHz)	Flash (KB)	SRAM (KB)	1/0	Supply voltage/ Operating temperature			complementary PWM		PWM	DAC	OPAMP	COMP	LPRCNT	TSC	USART/ISO7816	UART	LPUART	SPI/I <sup>2</sup> S	QSPI		USB Device	SDIO	DMA / PWM	SEGMENT LCD	뒤	DVP	Cryptographic algorithm	Package
	N32G432K8L7		108	64	24	26		10	1	17 6	1x12bit	10	1x12bit	_	_	_	_	2	2	1	2/2	_	2	1 1	_	1/8	_	-	_		LQFP32
z	N32G432KBL7		108	128	32	26		10	1	17 6	1x12bit	10	1x12bit	_	-	_	-	2	2	1	2/2	-	2	1 1	-	1/8	-	-	-		LQFP32
N32G432	N32G432C8L7		108	64	24	38		10	1 :	24 6	1x12bit	10	1x12bit	_	-	_	-	3	2	1	2/2	-	2	1 1	_	1/8	_	-	-		LQFP48
343	N32G432CBL7		108	128	32	38		10	1 :	24 6	1x12bit	10	1x12bit	_	_	_	-	3	2	1	2/2	-	2	1 1	_	1/8	-	_	_		LQFP48
N N	N32G432R8L7		108	64	24	52		10	1 :	28 12	1x12bit	16	1x12bit	_	-	_	-	3	2	1	2/2	-	2	1 1	_	1/8	_	-	_		LQFP64
	N32G432RBL7		108	128	32	52		10	1 :	28 12	1x12bit	16	1x12bit	_	-	_	-	3	2	1	2/2	-	2	1 1	-	1/8	-	-	-	DES/3DES、	LQFP64
	N32G435G8Q7		108	64	16	24		9	1	16 6	1x12bit	10	1x12bit	2	2	_	8	2	2	1	1/1	_	2		_	1/8	_	_	_	3DE	QFN28
	N32G435K8L7		108	64	16	26		9	1	17 6	1x12bit	10	1x12bit	2	2	_	8	2	2	1	2/2	_	2	1 1	_	1/8	_	_	-		LQFP32
Z <sub>3</sub>	N32G435KBL7		108	128	32	26		9	1	17 6	1x12bit	10	1x12bit	2	2	-	8	2	2	1	2/2	-	2	1 1	-	1/8	-	-	-	AES,	LQFP32
N32G435	N32G435C8L7		108	64	24	38		9	1	24 6	1x12bit	10	1x12bit	2	2	-	15	3	2	1	2/2	-	2	1 1	_	1/8	-	-	_	ν̈́	LQFP48
25	N32G435CBL7		108	128	32	38		9	1	24 6	1x12bit	10	1x12bit	2	2	_	15	3	2	1	2/2	_	2	1 1	_	1/8	_	-	-	ΙΑ1/:	LQFP48
	N32G435R8L7		108	64	24	52		9	1	28 12	1x12bit	16	1x12bit	2	2	-	20	3	2	1	2/2	-	2	1 1	_	1/8	_	-	-	SHA	LQFP64
	N32G435RBL7		108	128	32	52		9	1	28 12	1x12bit	16	1x12bit	2	2	_	20	3	2	1	2/2	_	2	1 1	_	1/8	_	-	-	224	LQFP64
	N32L433K8L7		108	64	24	26	<del></del>			17 6	1x12bit	10	1x12bit	2	2	_	8	2	2	1	2/2	-		1 1		1/8	-	-	_	SHA1/SHA224/SHA256、	LQFP32
	N32L433KBL7		108	128	32	26	8V~.			17 6	1x12bit	10	1x12bit	2	2	_	8	2	2	1	2/2	_		1 1	_	1/8	_	_	_	A25	LQFP32
7	N32L436C8L7	ARM	108	64	24	38	1.8V~3.6V	U		24 6	1x12bit	10	1x12bit	2	2	Y	15	3	2	1	2/2	-	2	1 1	_	1/8	4x20		_		LQFP48
1321	N32L436CBL7	Co	108	128	32	38		9		24 6	1x12bit	10	1x12bit	2	2	Υ	15	3	2	1	2/2	_	2	1 1	_	1/8	4x20		_	SM1	LQFP48
N32L43x	N32L436R8L7	ARM Cortex-M4F	108	64	24	52	-40~+105	9	1	28 12	1x12bit	16	1x12bit	2	2	Υ	20	3	2	1	2/2	-	2	1 1	-	1/8	4x34 8x30	_	-	SM1、SM3、SM4、	LQFP64
	N32L436RBL7	V4F	108	128	32	52	05°C	9	1	28 12	1x12bit	16	1x12bit	2	2	Υ	20	3	2	1	2/2	_	2	1 1	-	1/8	4x34 8x30		-	NS NS	LQFP64
	N32L436MBL7		108	128	32	64		9	1	28 12	1x12bit	16	1x12bit	2	2	Υ	24	3	2	1	2/2	-	2	1 1	_	1/8	4x44 8x40	-	-	14, 12, SI	LQFP80
	N32L401C8L7		64	64	16	38		9	1 2	24 6	1x12bit	10	1x12bit	2	2	-	15	3	2	1	2/2	_	2		-	1/8	-	-	-	SM7、	LQFP48
	N32L401CBQ7		64	128	16	38		9	1 :	24 6	1x12bit	10	1x12bit	2	2	-	15	3	2	1	2/2	-	2	-   -	-   -	1/8	-	-	-	MD5、	QFN48
	N32L403K8Q7		64	64	16	26		9	1	17 6	1x12bit	10	1x12bit	2	2	_	8	2	2	1	2/2	_	2	1 1	-	1/8	-	-	_	5,0	QFN32
	N32L403KBQ7		64	128	24	26		9	1	17 6	1x12bit	10	1x12bit	2	2	-	8	2	2	1	2/2	-	2	1 1	-	1/8	_	-	_	CRC16/CRC32、	QFN32
Z	N32L406C8Q7		64	64	16	38		9	1	24 6	1x12bit	10	1x12bit	2	2	_	15	3	2	1	2/2	_	2	1 1	_	1/8	4x20	_	_	16/0	QFN48
N32L40×	N32L406CBQ7		64	128	24	38		9	1	24 6	1x12bit	10	1x12bit	2	2	-	15	3	2	1	2/2	-	2	1 1	-	1/8	4x20	-	_	RC	QFN48
×0,	N32L406CBL7		64	128	24	38		9	1	24 6	1x12bit	10	1x12bit	2	2	_	15	3	2	1	2/2	_	2	1 1	_	1/8	4x20	_	_	32,	LQFP48
	N32L406R8Q7		64	64	16	52		9	1	28 12	1x12bit	16	1x12bit	2	2	-	20	3	2	1	2/2	-	2	1 1	-	1/8	4x34 8x30	-	_	TRNG	QFN64
	N32L406RBL7		64	128	24	52		9	1	28 12	1x12bit	16	1x12bit	2	2	-	20	3	2	1	2/2	_	2	1 1	-	1/8	4x34 8x30	-	_	d)	LQFP64
	N32L406MBL7		64	128	24	64		9	1	28 12	1x12bit	16	1x12bit	2	2	-	24	3	2	1	2/2	-	2	1 1	-	1/8	4x44 8x40	-	_		LQFP80

### General MCU

UCI	ierat MCO																																
	PC		Frec	П	St.		Sup Operat	Tin	ner	PW		ADC										Conr	iecti	vity					SE			Crypto	
Series	Commercial Product Code	Core	Frequency (MHz)	Flash (KB)	SRAM (KB)	1/0	Supply voltage/ Operating temperature	Timer	RTC	PWM	complementary PWM	Nb Resolution	PWM	DAC	OPAMP	COMP	LPRCNT	TSC	USART/ISO7816	UART	LPUART	SPI/I <sup>2</sup> S	QSPI	I²C	USB Device	CAN	SDIO	DMA / PWM	SEGMENT LCD	ETH	DVP	Cryptographic algorithm	Package
N32G020	N32G020G7QI		80	128	21	25	1.8V~5.5V -40~+85°	5	1	8	3	1x12bit	12	1x10bit	-	1	-	-	1	2	-	1/-	1	2	-	-	-	1/8	-	-	-	AES/DES/3DES, RSA/ ECC, SHA1/224/256/ 384/512, TRNG, CRC16	QFN32
020	N32G020K8QI		80	256	21	30	5.5V +85°C	5	1	8	3	1x12bit	12	1x10bit	-	1	-	-	1	2	-	2/-	1	2	1	-	-	1/8	-	-	-	DES、RSA/ /224/256/ NG、CRC16	QFN48
	N32G030F6U7		48	32	8	16		5	1	11	3	1x12bit	7	_	1	1	_	_	2	_	1	2/1	_	2	_	-	_	1/5	_	-	_		UFQFPN20
	N32G030F6S7		48	32	8	16		5	1	11	3	1x12bit	9	-	1	1	-	_	2	-	1	2/1	-	2	-	-	-	1/5	_	-	-		TSSOP20
	N32G030K6Q7		48	32	8	28		5	1	14	6	1x12bit	10	-	1	1	-	_	2	-	1	2/1	-	2	-	-	-	1/5	-	-	-		QFN32 (5mmx5mm)
N <sub>S</sub>	N32G030K6Q7-1		48	32	8	28		5	1	14	6	1x12bit	10	-	1	1	_	_	2	-	1	2/1	-	2	-	-	-	1/5	_	-	-	CRC1	QFN32 (4mmx4mm)
N32G030	N32G030K6L7		48	32	8	26		5	1	14	6	1x12bit	10	-	1	1	_	_	2	-	1	2/1	-	2	-	-	-	1/5	_	-	-	CRC16/CRC32	LQFP32
0	N32G030K8L7		48	64	8	26		5	1	14	6	1x12bit	10	-	1	1	_	_	2	-	1	2/1	_	2	_	_	-	1/5	_	-	_	C32	LQFP32
	N32G030C8L7		48	64	8	40		5	1	14	6	1x12bit	12	_	1	1	_	_	2	_	1	2/1	_	2	-	-	-	1/5	_	_	_		LQFP48
	N32G030C8T7	ARM	48	64	8	40		5	1	14	6	1x12bit	12	_	1	1	_	_	2	_	1	2/1	_	2	_	_	_	1/5	_	_	_		TQFP48
	N32G031F6U7	ARM Cortex-M0	48	32	8	16	•	5	1	11	3	1x12bit	7	_	1	1	_	_	2	_	1	2/1	_	2	_	_	_	1/5	_	_	_		UFQFPN20
23	N32G031F6S7	»-M	48	32	8	16		5	1	11	3	1x12bit	9	_	1	1	_	_	2	_	1	2/1	_	2	-	-		1/5	_	-	_	CRO	TSSOP20
N32G031x6	N32G031K6Q7	J	48	32	8	28		5	1	14	6	1x12bit	10	-	1	1	-	-	2	-	1	2/1	-	2	-	-	-	1/5	_	-	-	:16/C	QFN32 (5mmx5mm)
1x6	N32G031K6Q7-1		48	32	8	28	8V~5.	5	1	14	6	1x12bit	10	-	1	1	_	-	2	-	1	2/1	_	2	-	-	-	1/5	-	_	_	CRC16/CRC32	QFN32 (4mmx4mm)
	N32G031K6L7		48	32	8	26	.5V/-	5	1	14	6	1x12bit	10	-	1	1	-	-	2	-	1	2/1	-	2	-	-	-	1/5	-	-	-	, ,	LQFP32
	N32G031F8U7		48	64	8	16	1.8V~5.5V/-40~+105°C	5	1	11	3	1x12bit	7	-	1	1	-	_	2	-	1	2/1	-	2	-	-	-	1/5	_	_	-		UFQFPN20
z	N32G031F8S7		48	64	8	16	.05°C	5	1	11	3	1x12bit	9	_	1	1	-	_	2	-	1	2/1	_	2	_	-	-	1/5	-	-	-	Ω	TSSOP20
N32G031x8	N32G031K8Q7		48	64	8	28		5	1	14	6	1x12bit	10	-	1	1	_	-	2	_	1	2/1	-	2	_	-		1/5	-	-	-	CRC16/CRC32	QFN32 (5mmx5mm) QFN32
)31x	N32G031K8Q7-1		48	64	8	28		5	1	14	6	1x12bit	10	-	1	1	-	-	2	-	1	2/1	-	2	-	-	-	1/5	-	-	-	/CRC	(4mmx4mm)
6	N32G031K8L7		48	64	8	26		5	1	14	6	1x12bit	10	-	1	1	_	_	2	_	1	2/1	_	2	_	-	-	1/5	-	-	_	32	LQFP32
	N32G031C8L7		48	64	8	40		5	1	14	6	1x12bit	12	-	1	1	_	_	2	_	1	2/1	<del>-</del>	2	_	_	- -	1/5	-	-	_		LQFP48
	N32G031C8T7 N32G032F6U7		48	64 32	8	40 16	-	5	1	14	6 3	1x12bit 1x12bit	12 7	_	1	2	_		2	1	2	2/1 1/1		2		1		1/5 1/8	_		_		TQFP48 UFQFPN20
	N32G032F6S7		48	32	8	16		6		11		1x12bit	9	_		3	_	9	2	1	2	1/1	_	2	_			1/8	_	_	_	유	TSSOP20
_	N32G032P6W7		48	32	8	21			1	15	3	1x12bit	10	_	1	3	_	11	2	2	2	2/1	_	2	_	1		1/8	-	_	_	3C16	WLCSP25
N32G032	N32G032P8W7		48	64	16	21		6	1	15	3		10	-	1	3	-	11	2	2	2	2/1	_	2	-	1	- [	1/8	-	-	-	AES、SM4、CRC16/CRC32、	WLCSP25
303	N32G032K6Q7		48	32	8	28		6	1	17	6	1x12bit	10	_	1	3	-	17	2	2	2	2/1	-	2	_	1		1/8	-	-	-	SM, C32	QFN32
10	N32G032K6L7		48	32	8	26		6	1	17	6	1x12bit	10	_	1	3	-	15	2	2	2	3/2	-	2	_	1	-	1/8	-	-	-	, , <del>,</del> ,	
	N32G032C8L7		48	64	16	40			1	17	6	1x12bit	10	_	1	3	_	20	2	2	2	3/1	_	2	_	1		1/8	-	_	_	TRNG	LQFP48
	N32G032R8L7		48	64	16	56		6	1	17	6	1x12bit	16	-	1	3	-	24	2	2	2	3/1	_	2	-	1	-	1/8	_	-	_		LQFP64

注: 1<sup>1</sup>:Only Single Wire Y<sup>2</sup>:Only LCD Mode "-"means "not support"

### Bluetooth LE IC

	7.0		Fre	T.	<u> </u>		Supply Operating	Tin	ner		VM	ADC										Conn	ectiv	/ity								Crypto	
Series	Commercial Product Code	Core	Frequency (MHz)	Flash (KB)	SRAM (KB)	1/0	€ <	Timer	RTC	PWM	complementary PWM	Nb Resolution	PWM	DAC	OPAMP	COMP	LPRCNT	TSC	USART/ISO7816	UART	LPUART	SPI/I <sup>2</sup> S	QSPI	l²C	USB Device	SDIO	DMA / PWM	AMIC	IRC	BLE	DVP	Cryptographic algorithm	Package
N32WB020	N32WB020GEQI	ARM Cortex- M0+M0	80	320	20	17	1.8V~5.5V -40~+85°C	5	1	4	1	1x12bit	6	-	-	_	_	-	_	2	-	1/0	_	2	1 -		1/8	-	-	BLE4.2	-	CRC16	QFN32
N32W	N32WB031KCQ6-1 <sup>(1)</sup>	ARM Cortex M0	64	256	48	21	1.8V~3.6V -40~+85°C	4	1	8	6	1x10bit	8	-	-	-	-	_	2		1	2/2	-	1			1/5	1	1	В	-	CRC	QFN32
N32WB031	N32WB031KEQ6-2 (1)	Cortex-	64	512	48	21	2.32V~3.6V -40~+85°C	4	1	8	6	1x10bit	8	-	-	-	_	-	2	-	1	2/2	-	1		-   -	1/5	1	1	BLE5.1	-	CRC16/32	QFN32
Z	N32WB452CEQ6	ARI	144	512	144	30		8	1	23	6	2x12-bit	6	2x12-bit	-	-	-	6	3	2	_	3/2	-	2	1 :	2 2	2/16	-	-		-	SH,	QFN48
N32WB452	N32WB452REQ6	ARM Cortex- M4F+M0	144	512	144	44	1.8V~3.6V -40~+85°C	8	1	24	6	2x12-bit	11	2x12-bit	-	_	-	13	3	3	-	3/2	-	3	1 2	2 2	2/16	-	-	BLE5.0	Υ	AES/DES/3DES/ SHA/SM1/SM3/ SM4/SM7/MD5	QFN64
452	N32WB452LEQ6	tex-	144	512	144	68		8	1	24	6	2x12-bit	15	2x12-bit	-	-	_	16	3	4	-	3/2	-	4	1 2	2 2	2/16	-	-	0	Υ	3DES/ 'SM3/ 'MD5	QFN88

Note: (1) "-2" means the supply voltage 2.32-3.6V "-1" means the supply voltage 1.8-3.6V "·" means support "-" means "not support"

### Ultra Low Power Consumption Bluetooth IC

	RC	₽ Z		Frec		Sul		Co	onnectivi	ty	μ			10Bi		Trai	C	Power Consumption	on	Certification	
Series	ROM (Kbytes)	AM (Kbytes)	CPU Core	quency (MHz)	Standard	Supply voltage	GPIO	UART	l <sup>2</sup> C	SPI	32Bit TIMER	RTC	PWM	3it GPADC CH	Sensitivity	Transmit power	ShutDown	Sleep	Run (Typ)	Bluetooth SIG	Package
NZ8801	128	32	32-bit processor	32	BLE 5.0	1.62V~3.6V -40°C~85°C	17	1	1	1	4	1	3	3	-94dBm	Max +3dBm	<0.1μΑ	<1μΑ	Rx:3.5mA@3.0V Tx:3.6mA@3.0V	BQB Certification	QFN32 (4mm * 4mm)

### 5.8GHz high speed RF IC

		Frec	æ c	Data	(0	s PA	Trar		Pow Consun	er iption	
Series	Standard	Frequency (MHz)	Operating temperature	Data transfer rate	Wake up sensitivity	RF Receiving sensitivity	Transmit power	Standby	Wake up	Receive	Send
NWF580	GB/T 208512-2007	5.73GHz ~ 6.2GHz	-40°C∼85°C	256Kbps/ 512Kbps	-83dBm	-80dBm@ 调制系数85%	-6.1dBm ~ 8.4dBm	0.1μΑ	2μΑ	37mA	60mA@0dBm

# Contactless read/write IC

	Te	Con	necti	vity	ISO/I		Reac	Rece	Tra	Ca C. L	Rece	4	
Series	Operating Temperature	UART	I <sup>2</sup> C	SPI	ISO/IEC 14443-A/B	Туре	Read/write range	Receiving current	Transmitting current	Low power consumption card detection function	Receiving voltage	Transmitting voltage	Package
NZ3801	2.5V~3.6V -25°C~85°C	1.2Mbps	3.4Mbps	10MHz	106kbps/ 212kbps/ 424kbps/ 848kbps	Type A, Type B	100mm	9.5mA	60mA/ 100mA (max)	-	2.2V~3.6V	2.2V~3.6V	QFN32
NZ3802	2.5V~3.6V -25°C~85°C	1.2Mbps	3.4Mbps	20MHz (max)	/106kbps/ /212kbps/ /424kbps/ 848kbps	Type A, Type B	100mm	9.5mA	60mA/ 100mA (max)		2.2V~3.6V	2.2V~5V	QFN32

Note: "·" means support "-" means "not support"

### **Package Options**



#### **MCU Ecosystem**

#### **Development Board**

Minimum System Board











Full Function Development Board

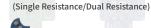


N32G457QE\_EVB



N32L436MBL7\_EVB





Smart Meter Development Board

Solution Development Board

Motor Drive Development Board



Smart Lock Development Board

#### **MP & Debug Tool**





Offline Programmer

Online Debug Tool

#### **Third Party Partners**

























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