

PRODUCT SELECTION GUIDE

DRIVEN BY INNOVATION

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Security IC
General MCU
ล Wireless RF

## Nations Technologies is committed to providing IC and solutions for people,

Making lives Safer, Simpler and Smarter



Established in 2000

## Qualifications and Honors

- Deputy Chairman member unit of China Association for Public Companies
- National High-tech Enterprise
- National-level postdoctoral programme

Leading enterprise in Shenzhen' s independent innovation industries
Shenzhen R\&D Center for Engineering and Technology Shenzhen R\&D Center for Information Security IC
Technology
Shenzhen Key Laboratory

## Service Capability

The Globalization of R\&D
Localized technical service team
Strategic partnerships with world-class wafer vendors

## Technical Competence

Own more than 1,400 international and domestic patents, including more than 1,000 invention patents Won the "China Patent Gold Award" in 2017. Won 9 of China Patent Excellence Awards for several years Own 60 technical standards. RCC technology had become a national standard in May,2017. Own the new generation trusted computing ISO / IEC


Technology Field:
Security, SoC, RF


## Marketing Strategy Of MCUs

## Sustainable Innovation, Providing More than $\mathbf{1 0 0}$ Product Models For Various Industries



## MCU Part Number Suffixes



## Security IC

|  |  |  |  |  |  |  |  |  |  | Timer |  | ADC |  |  |  |  |  |  |  | Con | necti | tivity |  |  |  | （HM） | cons | Power sumpt | tion | $\begin{array}{\|l\|l} \infty \\ \\ \end{array}$ |  |  |  | Certific | ication |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | $\circ$ <br>  <br>  |  |  | $\begin{aligned} & \text { 刀 } \\ & \mathbf{0} \\ & \mathbf{3} \\ & \underset{\underline{\alpha}}{ } \end{aligned}$ |  |  |  |  |  | PWM | $\begin{aligned} & z \\ & z \\ & 0 \\ & 0 \\ & 0 \\ & 0 \\ & \vdots \\ & \vdots \\ & 0 \\ & 0 \end{aligned}$ | ¢ |  |  |  | 58 |  | $\begin{array}{\|c} \overline{0} \\ \underset{\omega}{心} \\ \bar{\sigma} \end{array}$ |  | $\left\|\begin{array}{c} \infty \\ \frac{N}{2} \\ \bar{\omega} \end{array}\right\|$ | 欠is | c <br> 0 <br> O <br> 0 <br> 0 <br> $\vdots$ <br> $\vdots$ | － | O |  |  | $\begin{aligned} & \text { n } \\ & \stackrel{0}{0} \\ & \stackrel{\rightharpoonup}{0} \end{aligned}$ |  |  |  |  |  |  |  | NIST | USE |
|  | N32S032 | ARM Cort ex－ MO | 80 | 320 | － | － | 21 |  |  | 11 | 8 | 1x12bit | 12 | 1 | 5 | 1 | －－ | － | 1 | 3 | 2－ | 2 | 1 | 1／8 |  | － | 0．1uA | 80uA | $\begin{aligned} & 110 \mathrm{uA} \\ & / \mathrm{MHz} \end{aligned}$ | ． | AES／DES／3DES／SM1／SM4．RSA／ECC／SM2／SM9．SHA1／$224 / 256 / 384 /$$512 /$ SM3 | $\begin{gathered} \text { OFN48 } \\ \text { OFN32 } \\ \text { QFN20/SOP8 } \\ \text { OFN48 } \\ \text { QFN32 } \\ \text { SOP8 } \\ \hline \end{gathered}$ | LevelII EAL5＋ <br> Levelli EAL4＋ |  | － | $\begin{aligned} & \text { IPS } \\ & \hline 40-2 \\ & \text { AVP } \end{aligned}$ |  |
|  | N32S033 |  | 80 | 512 | － | － | 33 |  | 5 | 11 | 1 | 1x10bit | 10 | － 5 | 5 | 1 | －－ | － | 1 | 2 | 2／1 | 2 | 1 | 1／6 | $\pm 4$ | － | 0.14 A | 80uA | $\begin{aligned} & \text { 125uA } \\ & / \mathrm{MHz} \end{aligned}$ | － |  |  |  |  | － |  |  |
|  | Z32HUB | $\begin{aligned} & \text { ARM } \\ & \text { Cort } \\ & \text { ex- } \\ & \text { M0 } \end{aligned}$ | 60 | 320 | － | － | $16$ |  | 2 | 1 － | 1 | － | － | － | － 1 | 1 | －－ | － | － | 1 | 1／－ | － 1 | 1 | 2／1 | $\pm 4$ | － | $1 u \mathrm{~A}$ | 130uA | $\begin{aligned} & 500 \mathrm{uA} \\ & / \mathrm{MHz} \end{aligned}$ | ． |  | QFN32 | Levell｜EAL4＋ | － | － | $\begin{array}{l\|} \hline \text { FIPS } \\ 140-2 \\ \text { CAVP } \end{array}$ |  |
|  | Z32HUA | $\begin{aligned} & \text { ARM } \\ & \text { Cort } \\ & \text { ex- } \\ & \text { M0 } \end{aligned}$ | 80 | 512 | － | － |  |  | 5 | 11 | － | 1×12bit | 3 | 1 | －－ |  |  |  | 3 | 1 | $3_{1 /-}^{3 /-}$ | 1 | 1 | 1／8 | $\pm 4$ | － | 14 A | $\begin{aligned} & 130 \\ & \text { uA } \end{aligned}$ | $\begin{aligned} & 20 \\ & \mathrm{~mA} \end{aligned}$ | ． |  | QFN68 QFN32 | Levell｜EAL4＋ |  |  | $\begin{aligned} & \text { FIPS } \\ & 140-2 \\ & \text { CAVP } \end{aligned}$ |  |
|  | Z8IDA | $\begin{aligned} & \text { Zi80 } \\ & 51- \\ & \text { SC } \end{aligned}$ | 32 | － | 96 | 32 | 8 |  |  | －－ | － | 1x10bit | 3 | －－ | － | － | －－ | － | 1 | － | － 1 | 1 | －－ | － |  | － | － | $\begin{aligned} & 50 \\ & u A \end{aligned}$ | 4 mA | ． | DES／3DES／ SM4／SSF33， RSA／SM2， ECC，SHA1／／ SHA256／ SM3 | $\begin{aligned} & \text { DFN8 } \\ & \text { SOP8 } \end{aligned}$ | Levelll | － | － | － | － |
|  | Z8D16R－2 | $\begin{aligned} & \text { Zi80 } \\ & 51- \\ & \text { SC } \end{aligned}$ | 2 | 48 | － | － | $3.25$ |  |  | － 1 | － | 1×10bit | 3 | － | －－ | － | － | － | － | 1 | － | － | －－ | － | $\pm 4$ | － | $\begin{gathered} <0.8 \\ \text { uA } \end{gathered}$ | $\begin{aligned} & <2 \mathrm{uA} \\ & \text { @32.7 } \\ & 68 \mathrm{KHz} \end{aligned}$ | $\begin{gathered} <5 \mathrm{uA} \\ \text { @32.7 } \\ 68 \mathrm{Hzz} \\ \vdots \\ \vdots \\ <130 \\ \text { uA } \end{gathered}$ | ． | SM3 | Die | Level I | － | － | － | － |
|  | Z32HM | $\begin{aligned} & \text { M4K } \\ & \text { RISC } \end{aligned}$ | 60 | 1024 | － | － | 48 |  |  | －－ | － | － | － | －－ | － | － | － | － | 2 | 1 | 1／－ | － | －－ | 4 | － | － | － | $\begin{gathered} <100 \\ u A \end{gathered}$ | $\begin{gathered} 7 \mathrm{~mA} \\ @ \text { Core } \\ 60 \mathrm{MHZ} \\ 3.5 \mathrm{~mA} \\ @ \text { Core } \\ 30 \mathrm{MH} \\ \hline \end{gathered}$ | － | DES／BDES／AES／ <br> SM1／SM4／SM7 <br> ／SSF33，RSA <br> SM2，SHA $1 /$ <br> 224／256／SM3 | $\begin{aligned} & \text { WLCSP } \\ & \text { SOP8 } \\ & \text { DFN8 } \end{aligned}$ | Level｜IEAL4＋E | EAL4＋ | － | $\begin{aligned} & \text { FIPS } \\ & 140-2 \\ & \text { CAVP } \end{aligned}$ | － |
|  | Z32HCD2 <br> Z32HCD2S | $\begin{aligned} & \text { ARM } \\ & \text { Cort } \\ & \text { ex- } \\ & \text { M0 } \end{aligned}$ | 50 50 | － | 256 <br>  <br> 3208 | 40 | 11 <br> 11 |  | － | -- -- | － | － | － | －－ | - - - - | － | - <br> - <br> - | － | 1 | － | - - | - - - | 1 | ${ }_{-}^{-}$ | $\pm 4$ | $\pm 6$ $\pm 6$ | $\begin{gathered} <200 \\ \text { UA } \\ \hline \begin{array}{c} <200 \\ \text { UA } \end{array} \\ \hline \end{gathered}$ | $\begin{gathered} <200 \\ \text { uA } \\ <200 \\ \text { uA } \end{gathered}$ | 6mA | － | DES／3DES／ SM1／SM4／ SMT／／SF33， RSA／SM2，ECC， SHA1／256／ SM3 | Strip | Levelll－E | EAL4＋ |  |  | － |

Note：＂．＂means support＂－＂means＂not support＂

General MCU


[^0]General MCU


[^1]
## General MCU



[^2]
## Bluetooth LE IC



Note：（1）＂－2＂means the supply voltage 2．32－3．6V＂－1＂means the supply voltage $1.8-3.6 \mathrm{~V}$＂•＂means support＂－＂means＂not support＂

## Ultra Low Power Consumption Bluetooth IC

| ¢． |  |  | ก$\stackrel{\circ}{\complement}$$\stackrel{\circ}{\circ}$ |  | $\begin{aligned} & \text { 告 } \\ & \stackrel{訁}{2} \\ & \stackrel{1}{2} \end{aligned}$ |  | $\frac{\mathrm{O}}{\bar{O}}$ |  |  |  |  |  |  | $\begin{aligned} & \stackrel{\rightharpoonup}{\mathrm{o}} \\ & \stackrel{\rightharpoonup}{7} \\ & \stackrel{\rightharpoonup}{0} \\ & \stackrel{\rightharpoonup}{\mathbf{a}} \end{aligned}$ |  |  | Power Consumption |  |  | Certification |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  |  |  |  |  |  |  |  |  | $\stackrel{\pi}{7}$ | $\sum_{3}^{0}$ |  |  |  |  | $\begin{aligned} & \frac{n}{0} \\ & \frac{\$}{8} \end{aligned}$ | 年 |  |  |
| z © © | 128 | 32 |  | 32 | $\begin{aligned} & \text { 罣 } \\ & \stackrel{4}{0} \end{aligned}$ | $\begin{aligned} & 1.622 \sim 3.6 \mathrm{~V} \\ & -40^{\circ} \mathrm{C} \sim 85^{\circ} \mathrm{C} \end{aligned}$ | 17 | 1 | 1 | 1 | 4 | 1 | 3 | 3 | －94dBm |  | $<0.1 \mu \mathrm{~A}$ | $<1 \mu \mathrm{~A}$ |  |  | $\begin{gathered} \text { QFN32 } \\ (4 \mathrm{~mm} * 4 \mathrm{~mm}) \end{gathered}$ |

5.8 GHz high speed RF IC

|  |  |  |  |  |  |  |  | Power Consumption |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\begin{aligned} & \infty \\ & \frac{\infty}{\omega} . \\ & \frac{\omega}{\omega} \end{aligned}$ |  |  |  |  |  |  |  |  | $\begin{aligned} & \sum_{\infty}^{\mathbb{N}} \\ & \frac{1}{0} \\ & \frac{5}{0} \end{aligned}$ |  | ¢ $\stackrel{\text { ® }}{ }$ ® |
| $\sum_{\substack{N \\ 0}}^{2}$ |  | $\begin{aligned} & 5.73 \mathrm{GHz} \\ & 6.2 \widetilde{\mathrm{GHz}} \end{aligned}$ | $-40^{\circ} \mathrm{C} \sim 85^{\circ} \mathrm{C}$ |  | －83dBm |  | $\underset{\substack{-6.1 \mathrm{dBm} \\ \text { 8.dBm }}}{ }$ | $0.1 \mu \mathrm{~A}$ | $2 \mu \mathrm{~A}$ | 37 mA |  |

Contactless read／write IC

|  |  | Connectivity |  |  |  | － |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | $\underset{\substack{5 \\ \hline \\ \hline}}{ }$ | $\bar{\sim}$ | ¢ |  |  |  |  |  |  |  |  |  |
| NZ3801 | $\begin{aligned} & 2.5 \mathrm{~V} \sim 3.6 \mathrm{~V} \\ & -25^{\circ} \mathrm{C} \sim 85^{\circ} \mathrm{C} \end{aligned}$ | $\begin{aligned} & \stackrel{\rightharpoonup}{\tilde{n}} \\ & \text { 咅 } \end{aligned}$ |  | $\begin{aligned} & \stackrel{\rightharpoonup}{\mathbf{o}} \\ & \stackrel{y}{1} \\ & \hline \end{aligned}$ |  |  | 多 | $\begin{aligned} & \text { io } \\ & \text { in } \end{aligned}$ | $\begin{aligned} & \text { 3by } \\ & \text { Bex } \\ & \text { By } \end{aligned}$ |  | \％ | $\begin{aligned} & \underset{\omega}{\tilde{\omega}} \\ & \underset{\sim}{\tilde{N}} \end{aligned}$ | QFN3 |
| NZ3802 | $\begin{gathered} 2.5 \mathrm{~V} \sim 3.6 \mathrm{~V} \\ -25^{\circ} \mathrm{C} \sim 85^{\circ} \mathrm{C} \end{gathered}$ | $\begin{aligned} & \stackrel{\rightharpoonup}{\tilde{3}} \\ & \text { 苞 } \end{aligned}$ |  |  |  |  | $\begin{aligned} & \text { 응 } \\ & \text { B } \end{aligned}$ | 曾 |  |  | $\underset{\sim}{\tilde{\omega}}$ | N | QFN |

Note：＂•＂means support＂－＂means＂not support＂

## Package Options



## MCU Ecosystem

## Development Board

Minimum System Board


Full Function Development Board


N32G457QE＿EVB
N32L436MBL7＿EVB


Smart Lock Development Board

## MP \＆Debug Tool



Third Party Partners

| ARM | ※RTOS | ［－］阿里云 | －腾讯云 |
| :---: | :---: | :---: | :---: |
| RT－Thread | $\mathbf{M R T O S}_{\text {and stacks }}^{\mathbf{C}}$ | $\frac{\text { OIAR }}{\text { SYSIEMS }}$ |  |
| $\chi^{\text {T }}$ Technology | 阿莫智能设备 Amonis： | HHLO SYSTEMS | （成创社工方 |


[^0]:    注: $1^{1}$ :Only Single Wire $\quad Y^{2}$ :Only LCD Mode "-"means "not support"

[^1]:    注: $1^{1}$ : Only Single Wire Y2:Only LCD Mode "-"means "not support"

[^2]:    注: $1^{1}$ :Only Single Wire $\quad{ }^{2}$ : Only LCD Mode "-"means "not support"

